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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)	Application / Conf. No.	~~Unknown~~ 101 624,615
	Filing Date	July 21, 2003
	First Named Inventor	Jason H. Anderson
	Art Unit	~~Unknown~~ 2825
	Examiner Name	~~Unknown~~ Siek, V.
Sheet 1 of 1	Attorney Docket Number	X-945 US

OTHER – NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
VS		Vaughn Betz, Jonathan Rose; "FPGA Routing Architecture: Segmentation and Buffering to Optimize Speed and Density"; 10 pages.	
VS		Dijkstra, E. ; "A Note on Two Problems in Connexion with Graphs, Numerische Mathematik 1; 1959; 2 pages. [attached publication from book: 25.2 Dijkstra's algorithm - pp.527-531.	
VS		L. McMurchie, C. Ebeling; "PathFinder: A Negotiation-Based Performance-Driven Router for FPGAs"; Proc. of the February 1995; 1995 ACM Third International Symposium on Field-Programmable Gate Arrays Aided Design; 7 pages.	

Examiner Signature	VUTHE SIEK	Date Considered	5/3/2005

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.